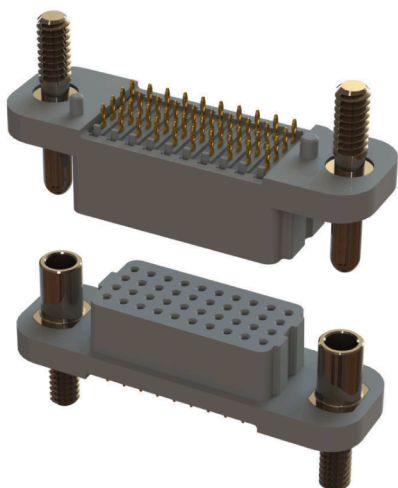




verSI

verSI, 1.27mm (0.050")



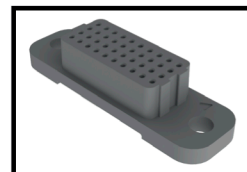
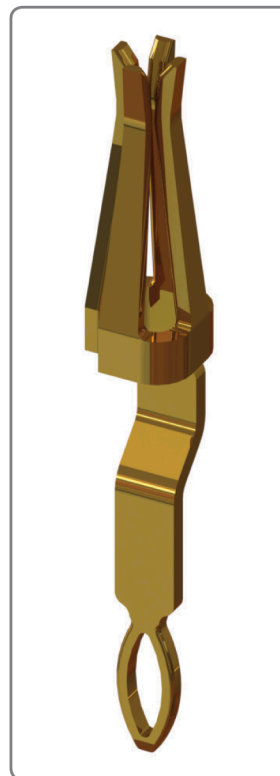
OPEN-PIN FIELD CONNECTOR FAMILY

The AirBorn verSI (verSI = versatile connectors with high-speed Signal Integrity) open-pin field product line is designed to meet the requirements for high-speed/signal integrity applications while still delivering the reliability customers have come to expect from AirBorn. The AirBorn verSI product family delivers flexibility in design by offering vertical board-mount, right angle board-mount, cable I/O, and flex circuit mounting with 40 to 500 contacts. Vertical board-mount plugs and mating vertical receptacles also support board-to-board stacking applications. Board spacing ranges from 8mm to 25mm. EMI hoods are available for best-in-class ruggedness and durability. Bolt-down pads allow for superior robustness and worry-free mating and unmating.

The AirBorn verSI offers several board termination types including paste-in-hole, thru hole, and compliant pin press fit technology, which eliminates the need for costly X-ray inspection.

FEATURES and BENEFITS

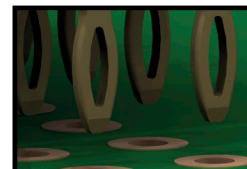
- Four points of contact offers superior performance and reliability
- Versatile product offering including vertical, right angle, and cable
- Rugged and EMI shrouds available
- Low mating forces – less than 40 grams
- Contact termination options:
 1. Press-fit
 2. Paste-in-hole
 3. Thru-hole
- Stack heights available from 8mm to 25mm in 1mm increments
- Open-pin field design allows for flexibility in termination schemes (single-ended, differential pair, power and ground or both)
- 100 Ω differential impedance matching with G_S+_S-_G signal layout



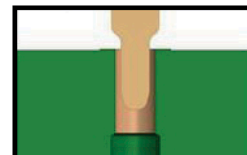
VSF VERTICAL CONNECTOR



FLEX JUMPERS



PRESS-FIT TERMINATIONS



PASTE-IN-HOLE

AirBorn International Ltd

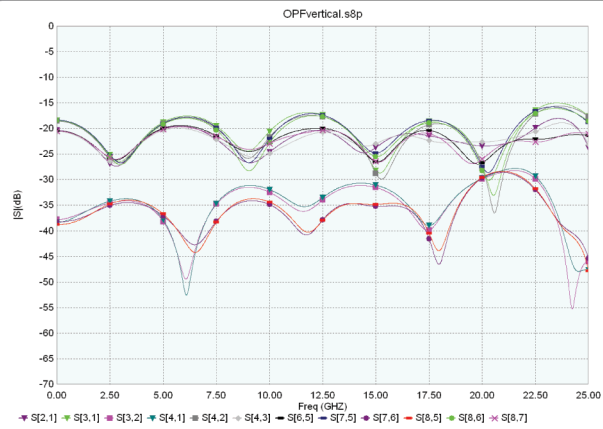
Mercury House
Station Road
Edenbridge, Kent
TN8 6HL
United Kingdom

T: +44 (0)1732 864930 E: cs@airbornint.com

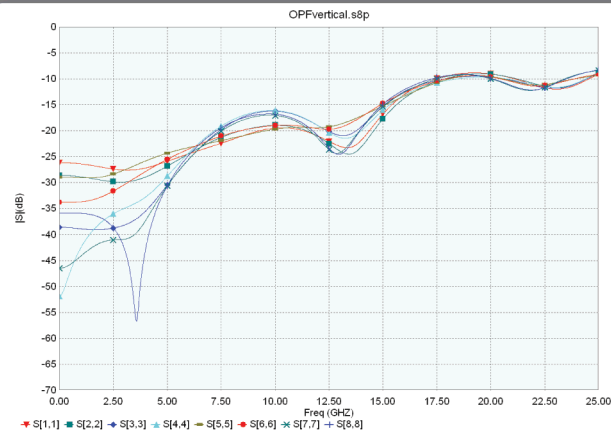
www.airborn.com

PATENT PENDING

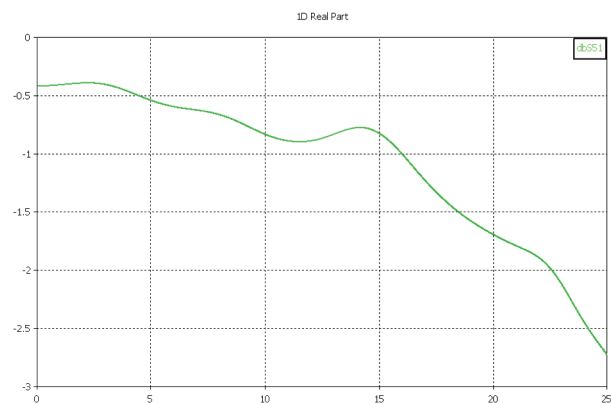
TYPICAL APPLICATION and SI PERFORMANCE FOR MATED PAIR



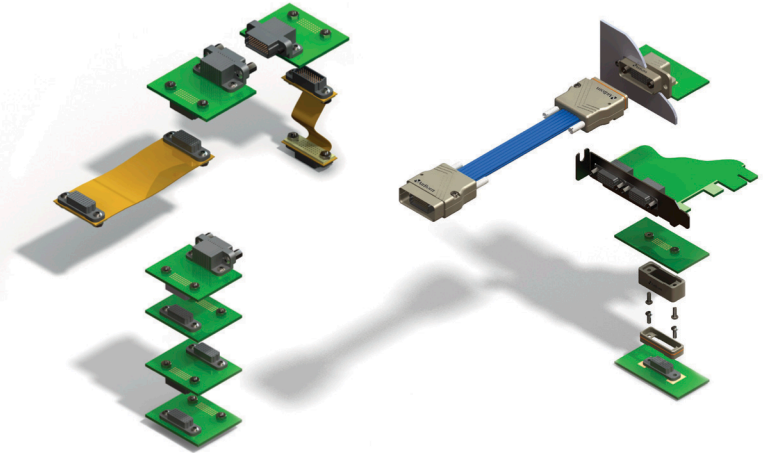
FAR/NEAR END CROSSTALK



RETURN LOSS

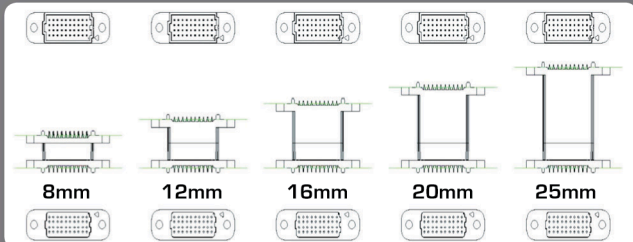


INSERTION LOSS

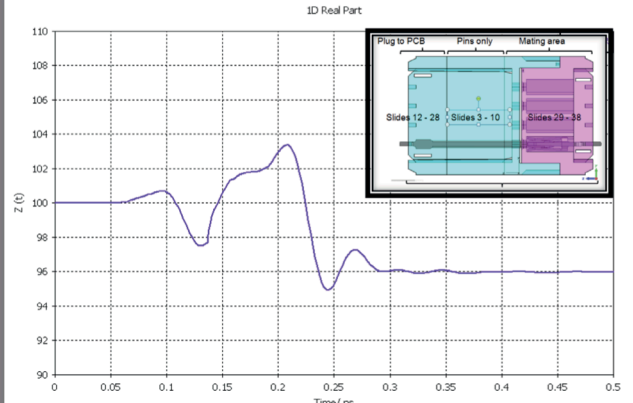


verSI					
	10 pin / Row	20 pin / Row	30 pin / Row	40 pin / Row	50 pin / Row
4 Row	X	X	X	X	X
5 Row	X	X	X	X	X
6 Row	X	X	X	X	X
8 Row	X	X	X	X	X
10 Row	X	X	X	X	X

ARRAY OPTIONS



STACK HEIGHT OPTIONS



DIFFERENTIAL IMPEDANCE PROFILE

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PATENT PENDING